

Position: Principal Digital Design Engineer
Location: Ireland/UK/Europe

Analog Devices, Inc. is a leading global high-performance technology company dedicated to solving our customers' most complex engineering challenges. We play a critical role at the intersection of the physical and digital world by providing the building blocks to sense, measure, interpret, connect, and power devices and systems. We design, manufacture, test, and market a broad portfolio of solutions, including integrated circuits (ICs), software and subsystems that leverage high-performance analogue, mixed-signal and digital signal processing technologies. We embrace a culture of innovation and collaboration to push the state of the art.

Position Overview:

ADI has long been considered the industry leader in high performance analogue/mixed signal semiconductors, specializing in bridging the real world to the digital world: what is increasingly referred to by the technical community as the "Intelligent Edge".

As ADI continues to grow its capability in edge processing technology, we are building a new team to develop digital chiplets and are looking for a **Principal Digital Design Engineer** to lead the design and development of a digital compute engine chiplet. This is an opportunity to join a young, multi-functional engineering team to build a new strategy creating compute subsystems that will allow ADI's product teams to develop products that are uniquely different than anything that you will find on the market today. Our development team is distributed around the globe, so you will be able to work from any existing ADI locations.

Responsibilities:

- Technical leadership in defining an SoC system;
- Creation of a detailed development plan driving design specification and microprocessor subsystem architecture;
- Work closely with the Verification & Embedded S/W teams during development;
- Driving the digital design flow to the completion of a design to tape-out;
- Support building out the development team.

Minimum Qualifications:

- Experience leading digital chip architecture and/or digital design function;
- Degree in Electrical/Electronic Engineering, Computer Science, or Computer Engineering, with 12+ years of experience;
- Strong engineering background in embedded system design including SoC architecture, computer architecture, and custom or standard DSP;
- Deep understanding of both SoC design flow and embedded software development processes;
- Detailed knowledge of ARM and/or RISC-V processor subsystem architectures, on-chip interconnects, and advanced debug and trace;
- Experience in FPGA emulation, including leading pre-silicon system validation, prototype bring-up, debug, validation, characterization, and module integration support;
- Experienced working with geographically distributed design teams;
- Excellent written and verbal communication skills and solid teamwork and leadership skills;
- Experience with silicon and software product development and understanding the product development lifecycle.