Position: Principal Digital Design Engineer
Location: Ireland/UK/Europe

Analog Devices, Inc. is a leading global high-performance technology company dedicated to solving our customers' most complex engineering challenges. We play a critical role at the intersection of the physical and digital world by providing the building blocks to sense, measure, interpret, connect, and power devices and systems. We design, manufacture, test, and market a broad portfolio of solutions, including integrated circuits (ICs), software and subsystems that leverage high-performance analogue, mixed-signal and digital signal processing technologies. We embrace a culture of innovation and collaboration to push the state of the art.

Position Overview:
ADI has long been considered the industry leader in high performance analogue/mixed signal semiconductors, specializing in bridging the real word to the digital world: what is increasingly referred to by the technical community as the "Intelligent Edge".
As ADI continues to grow its capability in edge processing technology, we are building a new team to develop digital chiplets and are looking for an **Experienced Digital Physical Design / Implementation Engineer** to join the group in the development of a digital compute engine chiplet. This is an opportunity to join a young, multi-functional engineering team to build a new strategy creating compute subsystems that will allow ADI's product teams to develop products that are uniquely different than anything that you will find on the market today. Our development team is distributed around the globe, so you will be able to work from any existing ADI locations.

Responsibilities
- Digital Implementation of the digital cores required of our mixed-signal IC developments, from RTL to GDSII.
- Creation of efficient and effective physical implementation flows, building on ADI's substantial in-house capabilities.
- Collaborate with internal and contractor project resources to achieve project milestones
- Keeping informed of and adopting the latest CAD innovations in this challenging area. As our products become more complex, our implementation flows should evolve accordingly.
- Work within the wider ADI digital implementation community to proliferate & share best practice methodologies and technologies

Minimum Qualifications:
- BEng/MEng degree in Electronics Engineering with 8+ years relevant digital ASIC design experience

Required Skills/Experience:
- Logic Synthesis, Place & Route, Clock Tree Synthesis, ECO methodologies, and Physical Verification.
- Static Timing Analysis, Logic Equivalence Checking and Power Analysis Flows.
- Knowledge of digital implementation tools and flows essential.
- Experience in 16nm and smaller geometry process technologies
- Strong experience in the development and execution of schedules for all digital implementation tasks.
- Demonstrated teamwork involving engineers working in geographically disparate locations.
- Strong problem-solving skills with an ability to understand and clearly articulate technical issues.
- Good supplementary skills in scripting languages (such as TCL, Perl, Python, or similar) an advantage.
- Proven experience in productively engaging with cross-functional, global development teams with strong sense of responsibility to deliver on project milestones