





Nov 18th, Dunboyne Castle Hotel - *Conference and Gala Dinner* "Digital Transformation enabling a sustainable world"





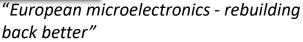


Dr. Cara Augustenborg
Fellow at UCD
Broadcaster NewsTalk
"Building a Better World:
Focus on Sustainability"





Colette Maloney Head of Unit "EU Commission







Dr. Ruoyi Zhou, Director of IBM Research
"Quantum Computing: Cutting Through The Hype!"



MICROCHIP
Sudarshan Iyengar,
VP, Ireland Development Centre
"Intelligent Electronics for Good"





Prof Lorraine Leeson , Deaf Studies "Sign of the Times - Technology and Sign Language"



Prof JP Colinge "Silicon Integration; Beyond Scaling"



intel.

John Rayfield, VP & GM VPU IP "Low Power is everything in AI"



Dunboyne Castle Hotel



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"CEO Forum"
With Leo Clancy
CEO Enterprise Ireland
November 17th





" Women in Tech, a roadmap for Microelectronics " chaired by Siobhan Dolan Clancy

- Prof Orla Feely, VP Research, Innovation and Impact, UCD
 - Brendan Farley, Vice President Wireless Engineering, Managing Director EMEA at Xilinx
 - Jennifer Hoffman Senior Director, People & Culture, Integration Management at Analog Devices
 - Neil Philip, Factory Manager at Intel Ireland



Gala Dinner
Recognising the 10 founding
members



	09:15am	Leonard Hobbs - General Manager's Welcome and Address
ı	09.45am	Guest Speaker on Climate – Dr. Cara Augustenborg "Building a Better World: Focus on Sustainability"
ı	10:30am	Sudarshan Iyengar, Vice President, Ireland Development Centre, Microchip "Intelligent electronics for good"
ı	11.00am	Dr. Giorgos Fagas, Head of EU Programmes, Tyndall National Institute, Introducing Ascent+ - Member Opportunities
ı		in Europe's leading technology providers
ı	11.30am	Networking Coffee Break
	12:00pm	Dr. Ruoyi Zhou, Director of IBM Research "Quantum Computing: Cutting Through The Hype!"
	12.30pm	Colette Maloney, Head of Unit, Microelectronics and Photonics, DG Connect, EU Commission "European microelectronics - rebuilding back
		better"
	1.00pm	Networking Lunch Break
	2:00pm	Professor Lorraine Leeson, Professor in Deaf Studies at TCD, "Sign of the Times - Technology and Sign Language"
	3.00pm	John Rayfield, VP & GM VPU IP at Intel "Low power is everything in AI"
	3:30pm	Professor JP Colinge - "Silicon Integration: Beyond Scaling"
	4:00pm	Networking Coffee Break
	4:30pm	Panel discussion. "Women in Tech, a roadmap for Microelectronics", chaired by Siobhan Dolan Clancy, with Prof Orla Feely, VP Research,
		Innovation and Impact, UCD, Brendan Farley, Vice President Wireless Engineering, Managing Director EMEA at Xilinx, Jennifer Hoffman Senior
		Director, People & Culture, Integration Management at Analog Devices and Neil Philip, Factory Manager at Intel Ireland.
	5.00pm	Closing Remarks
	7.00pm	Evening Reception
	7.30pm	Dinner and celebration of 21 years of MIDAS

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